

**Office of the Dean Research and Consultancy
Indian Institute of Engineering Science & Technology (IEST), Shibpur
Howrah-711 103**

Project Title: "Development of Cyber-Resilient Electric Vehicle Charging Infrastructure and Security Control Framework with Fault Diagnosis and Vulnerability Analysis facilities"
[Sponsoring Authority: Anusandhan National Research Foundation (ANRF), Govt. of India]

[Institute Project Code: DRC/ANRF (4211)/EE/AB/025/25-26]

**Department of Electrical Engineering,
Indian Institute of Engineering Science & Technology (IEST), Shibpur
Howrah-711 103**

Ref.: Advt. No. EE 1924, dated 16.04.2026

**Principal Investigator: Dr. Atanu Banerjee, Associate Professor, EE Deptt.
Co-Investigator: Dr. Amal Barman, Associate Professor, EE Deptt.**

Interested candidates are requested to appear for a walk in-interview on **27st April 2026, 11 am onwards** at **Sub-Library of EE Department, 1st Floor, Academic Building** for the following temporary post in the department of Electrical Engineering, **Indian Institute of Engineering Science & Technology (IEST), Shibpur, Howrah-711103.**

Junior Research Fellow: No of post - One

Essential Qualification: (i) Master's degree (M.E/M.Tech) in Electrical Engineering/Electronics and Communication Engineering/Computer Science & Engineering/Information Technology or allied discipline from a recognized University/ Institute with minimum 60% marks (6.5 CGPA on a ten-point scale) in the qualifying degree. Or (ii) B.E/B.Tech in Electrical Engineering/Electronics and Communication Engineering/Computer Science & Engineering/Information Technology or allied discipline with a minimum of 80% aggregate marks (or 8.5 CGPA on a ten-point scale) in the qualifying degree and a valid GATE score with a minimum of 80 percentile. Marks relaxation for SC/ST/OBC (NCL)/DA candidates will be allowed as per Gol norms.

Candidates who are in final semester of respective program may also apply but if selected their admission will be subjected to the submission of final certificate.

Desirable Qualification: It is highly desirable that the candidates have research experience in modelling, design and control of Power Electronic Converters (mainly dc-dc and dc-ac converters), integration of renewable energy sources with grid etc. and hands on experience in developing and working on hardware / laboratory prototype of the converters. Knowledge of simulation softwares like MATLAB (programming/SIMULINK) is also highly desirable. Candidates must be well conversant to programming with different digital controllers like FPGA/Microcontroller/DSP. Relevant experience of working with IoT sensors will be highly appreciated.

Job Profile: The responsibility of the JRF will be to explore the opportunities and challenges of integrating Grid Emulator with Electric Vehicle Supply Equipment [EVSE] and EV Battery Management Systems [BMS]. Different types of cyber-attacks to be generated based on cloud-based data and through IoT sensors the same to be transferred to EVSE and Grid Emulator. Artificial Intelligence [AI] based control to be exercised to counter cyber-attacks on Grid and EV charging infrastructures. One smart & intelligent control module to be developed which can be installed across EV charging stations.

Fellowship: 1st & 2nd Year: Rs. 37,000/- p.m. + Rs 11,100/- (30% HRA) = Rs 48,100/- per month; 3rd Year: Rs. 42,000/- p.m. + Rs 12,600/- (30% HRA) = Rs 54,600/- per month. Medical facilities are available with institute doctor and two hospitals.

Age Limit: Maximum 32 years on day of interview (Upper age limit to be relaxed up to 5 years for SC/ST/OBC/DA candidates).

Duration of Appointment: Initially the appointment will be made for one year with can be extended up-to 3 (three) years purely based on the performance. The position is co-terminus with the project.

Important Information:

1. Candidate possessing the requisite qualification and experience as mentioned above should reach the venue by 11.00 am on the day of interview for document verification.
2. Soft copy of the bio-data may be sent through e-mail within 20th April,2026 to: Dr. Atanu Banerjee, PI at E-mail id: atanu.ee@faculty.iiests.ac.in.
3. No TA/DA shall be paid to candidates for attending the Interview and/or joining the position.
4. Original documents and two sets of self-attested copies of age proof/certificates/degrees/mark-sheets and other testimonials must be presented at the time of interview for verification.
5. The appointment is for time bound project and the candidate is required to work dedicatedly for the successful completion of the project. Selected candidate has to join immediately and **may enrol for Ph.D program** as per rules of IEST Shibpur.
6. Mere possession of minimum qualification does not guarantee an invitation for the interview. The authority reserves the right of accepting/rejecting any or all applications without assigning any reason thereof.
7. All the Terms and Conditions for this recruitment will be as per guidelines of Anusandhan National Research Foundation [ANRF], Govt. of India. Office Orders issued vide ANRF/OM/N-01/2024 dated 14th October,2024 and SB/S9/Z-06/2019 dated 23rd April,2019 are relevant.

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CORRIGENDUM

Essential Qualification: Engineering/Electronics and (i) Master's degree (M.E/M.Tech) in Electrical Communication Engineering/Computer Science & Engineering/Information Technology or allied discipline from a recognized University/ Institute with minimum 60% marks (6.5 CGPA on a ten-point scale) in the qualifying degree and a valid GATE score. Or (ii) B.E/B.Tech in Electrical Engineering/Electronics and Communication Engineering/Computer Science & Engineering/Information Technology or allied discipline with a minimum of 80% aggregate marks (or 8.5 CGPA on a ten-point scale) in the qualifying degree and a valid GATE score with a minimum of 80 percentile. Marks relaxation for SC/ST/OBC (NCL)/DA candidates will be allowed as per Gol norms. Candidates who are in final semester of respective program may also apply but if selected shall be allowed for provisional admission on production of course completion certificate from appropriate authorities. They shall be required to submit their results of the qualifying degree examinations and migration certificate (as applicable) by 30.09.2026 and failing which their admission shall stand cancelled.

Important Information:

1. Candidate possessing the requisite qualification and experience as mentioned above should reach the venue by 10.00 am on the day of interview for document verification.
2. Soft copy of the bio-data may be sent through e-mail within 7th May,2026 to: Dr. Atanu Banerjee, PI at E-mail id: atanu.ee@faculty.iiests.ac.in.

Revised date of walk in-interview is 12th May 2026 on 11.00 am onwards. The other terms and conditions remain unchanged.


Dean (R & C)

(W. Code DRC-005/26-27)

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